

PATENT NUMBER

A O.I.P.E. SCANNED <u>117 (C)</u> Q.A. <u>ST</u>	PATENT DATE
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APPLICATION NO. 09/648164	CONT/PRIOR	CLASS 257	SUBCLASS 327	ART UNIT <del>2811</del> 2826	EXAMINER <i>Dickens</i>
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## APPLICANTS

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### TITLE

Architecture for circuit connection of a vertical transistor

PTO-2040  
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ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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